

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

REPLY TO OFFICE ACTION DATED 04/11/2008

Atty. Docket No.
SUZU1720

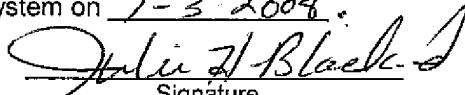
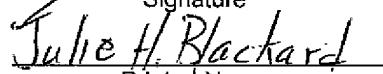
Applicant Takeshi Ishigaki	
Application Number 10/586,951	Date Filed 07/25/2006
Title Semiconductor Integrated Circuit Device and Method of Outputting Signals on Semiconductor Integrated Circuit	
Group Art Unit 2816	Examiner Rojas, Daniel E.
Confirmation Number: 3389	

Certificate of Transmission Under 37 C.F.R. § 1.8

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that this correspondence is being deposited electronically with the U.S. Patent and Trademark Office using the United States Patent and Trademark Office's EFS-Web system on 7-3-2008.


Signature

Printed Name

In response to the Official Action mailed April 11, 2008, Applicant respectfully requests the Examiner reconsider the rejections of the Claims in view of this reply.